

011 U.S. F
09/987566

11/15/01

327	77
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE

SCANNED LR(4) Q.A. W

04-2120

APPLICATION NO. 09/987566	CONT/PRIOR D F	CLASS 327	SUBCLASS 541	ART UNIT 2816	EXAMINER Cunningham
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APPLICANTS



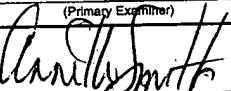
Fukashi Morishita

FILE

Internal power supply voltage generation circuit that can suppress reduction in internal power supply voltage in neighborhood of lower limit region of external power supply voltage

PTO-2040

PTO-204
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 10	Figs. Drwg. 17	Print Fig. 4	Total Claims 2	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	 _____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
				6/3/05	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	 Terry D. Cunningham Primary Examiner _____ (Primary Examiner) 5/3/05 _____ (Date)			ISSUE FEE	
				Amount Due \$1,700 Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	 _____ (Legal Instruments Examiner) 6/6/05 _____ (Date)			ISSUE BATCH NUMBER	

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